AGC to Further Expand its Supply System for EUVL Mask Blanks

Tokyo, November 28, 2018 - AGC (Headquarters: Tokyo; President: Takuya Shimamura) has decided to further expand its production capacity of EUV lithography photomask blanks ("EUVL mask blanks") at its group company AGC Electronics (Headquarters: Koriyama, Fukushima Pref.; President: Hiromasa Sato). This expansion is in addition to the expansion announced earlier this year on February 5, and will enable AGC Group to triple its EUVL mask blank production capacity by 2020.

To respond to the approaching IoT era, in which all manner of things will be connected via internet, the sophistication and miniaturization of electronic devices will continue to advance, creating demand for semiconductor chips with faster calculation speeds, larger data capacity, and more advanced integration. The circuit patterns of semiconductor chips must be further miniaturized to achieve these goals, yet with currently available optical lithography technology\(^1\) it is theoretically unrealistic to fabricate miniaturized patterns at what is commonly termed a “7nm” level. Therefore, EUV\(^2\) lithography is considered the most promising technology to replace optical lithography.

AGC began conducting R&D on photomask blanks utilized in EUV lithography technology in 2003. By combining its core technologies (i.e. glass materials, glass polishing, and film coatings), AGC is currently the only manufacturer in the world capable of handling every technology required for photomask blanks—from the glass material to film material.

In anticipation of the expansion of EUV lithography, which is expected to become the prevailing technology, AGC has decided to further expand the supply system for EUVL mask blanks at one of its group companies, AGC Electronics.

Under its **AGC plus** management policy, the AGC Group has made a commitment to positioning electronics related business as one of its key strategic initiatives. AGC intends to continue making aggressive capital investment in EUV mask blanks, which are expected to see significant growth in demand in the coming years, to contribute to further development of the semiconductor industry.

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**MEDIA INQUIRIES**

Kazumi Tamaki, General Manager, Corporate Communications & Investor Relations Division

AGC Inc.

(Contact: Ai Ota; Tel: +81-3-3218-5603; E-mail: info-pr@agc.com)

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- NOTES -

1: Optical Lithography Technology: The process of transferring LSI circuit patterns to silicon wafers, etc., using a KrF (krypton fluoride) or ArF (argon fluoride) light source.

2: EUV: Extreme ultraviolet radiation with a wavelength of 13.5nm.

- REFERENCE -

■ About AGC Electronics

<table>
<thead>
<tr>
<th>Official name</th>
<th>AGC Electronics Co., Ltd.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Capital</td>
<td>300 million yen</td>
</tr>
<tr>
<td>Representative</td>
<td>Hiromasa Sato</td>
</tr>
<tr>
<td>Head office location</td>
<td>Koriyama City, Fukushima Prefecture</td>
</tr>
<tr>
<td>Employees</td>
<td>Approximately 600 (as of end of December 2017)</td>
</tr>
<tr>
<td>Primary business</td>
<td>Semiconductor-related materials business, optical device related business, etc.</td>
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</tbody>
</table>

■ About EUV Mask Blanks

EUVL mask blanks are a low-thermal expansion glass substrate with various kinds of optical coating films on its surface. An EUV photomask comprises a semiconductor chip circuitry pattern formed onto the surface of an EUVL mask blank, and this circuit is transferred onto a silicon wafer in order to create a semiconductor chip.

Due to the further miniaturization of circuits, EUVL mask blanks are increasingly expected to:

- Have as close to zero micro-defects as possible;
- Have almost perfect flatness

Outline view of the EUV lithography process

![Diagram of EUV lithography process](image)

**13.5nm**

**Wave length**

**Target**

**Laser plasma light source (EUV light source)**

**EUVL mask**

**Wafer stage**

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